ASIC Project Spec Intake (48h indicative range)

Use this form to share just enough info for an initial range. Nothing here is a commitment; we can proceed under a mutual NDA for details.

A) Basics

Company / Project	
Contact / Email	
Expected Volume Tier	
Target Launch (quarter)	

B) Workload & KPIs

Primary use-case	
Throughput / Latency targets	
Power / Thermal budget	
Die size budget (if any)	

C) Architecture hints

Interfaces (I/O)	
On-chip blocks (DSP/accelerators/CPU/memories)	
Security / Safety requirements	

D) Implementation constraints

Preferred node / package	
Test constraints (ATE time, coverage)	
Compliance (automotive, medical, industrial)	

E) Existing assets

Reusable RTL / IP available?	Yes / No / In progress
FPGA prototype available?	Yes / No / Planned
Links / Attachments	

We’ll respond within 48 hours with a non-binding range based on node, die, package and test assumptions. For specifics, we’ll proceed under an NDA.